

**APPARATUS AND METHOD FOR PRODUCING AN OUTPUT CLOCK PULSE  
AND OUTPUT CLOCK GENERATOR USING SAME**

**ABSTRACT**

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**[0106]** A method and apparatus generates output clock pulses, having leading and trailing edges that are adjusted in a pulse forming processor, according to the relative phase of an output clock and output data. Dynamic adjustment of the leading and trailing edges of output clock pulses improves the performance of high-speed devices  
10 significantly.